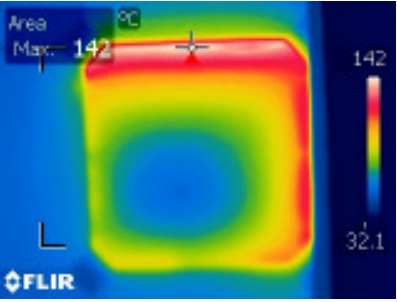


<b>10.9 B</b>	<b>TABLE: Hot-spot endurance test</b>						P																																																											
Test Date (MM/DD/YYYY) start/end .....	11/23/2012						—																																																											
Cell interconnection circuit.....	<input checked="" type="checkbox"/> S <input type="checkbox"/> SP <input type="checkbox"/> SPS						—																																																											
Module temperature at thermal equilibrium [°C] .....	51,6						—																																																											
<b>Determination of worst case cell</b>							—																																																											
Maximum measured cell temperature in 5 cycles [°C].....	142,0						—																																																											
Shading rate [%] .....	90						—																																																											
Supplementary information: Worst Cell Number 40																																																																		
<table border="1" style="display: inline-table; vertical-align: top;"> <tr><td>1</td><td>2</td><td>3</td><td>4</td><td>5</td><td>6</td><td>7</td><td>8</td><td>9</td><td>10</td></tr> <tr><td>11</td><td>12</td><td>13</td><td>14</td><td>15</td><td>16</td><td>17</td><td>18</td><td>19</td><td>20</td></tr> <tr><td>21</td><td>22</td><td>23</td><td>24</td><td>25</td><td>26</td><td>27</td><td>28</td><td>29</td><td>30</td></tr> <tr><td>31</td><td>32</td><td>33</td><td>34</td><td>35</td><td>36</td><td>37</td><td>38</td><td>39</td><td>40</td></tr> <tr><td>41</td><td>42</td><td>43</td><td>44</td><td>45</td><td>46</td><td>47</td><td>48</td><td>49</td><td>50</td></tr> <tr><td>51</td><td>52</td><td>53</td><td>54</td><td>55</td><td>56</td><td>57</td><td>58</td><td>59</td><td>60</td></tr> </table> 							1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40	41	42	43	44	45	46	47	48	49	50	51	52	53	54	55	56	57	58	59	60
1	2	3	4	5	6	7	8	9	10																																																									
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41	42	43	44	45	46	47	48	49	50																																																									
51	52	53	54	55	56	57	58	59	60																																																									
Above picture depicts the identification of the numbered single cells viewed from the front side of the module. Shaded area marks the junction box location on the backsheet, behind of the module.																																																																		
<b>(10.1 Visual inspection after hot-spot endurance test)</b>							P																																																											
Test Date (MM/DD/YYYY).....	11/26/2012						—																																																											
Sample #	Nature and position of findings – comments or attach photos						—																																																											
2	No findings						P																																																											
Supplementary information:																																																																		
<b>(10.2 Maximum power determination after hot-spot endurance test)</b>							P																																																											
Test Date (MM/DD/YYYY).....	11/27/2012						—																																																											
Module temperature (°C).....	25						—																																																											
Irradiance (W/m <sup>2</sup> ).....	1000						—																																																											
Sample #	Voc (V)	Vmp (V)	Isc (A)	Imp (A)	Pmp (W)	FF (%)																																																												
2	38,9	30,9	9,43	8,77	271	73,9																																																												
Pmp degradation after this test (%) ≤5%:	+0,2						P																																																											
Supplementary information:																																																																		
<b>(10.3 Insulation test after hot-spot endurance test)</b>							P																																																											
Test Date (MM/DD/YYYY).....	11/26/2012						—																																																											
Test Voltage applied (V, DC) .....	1000 / 6000						—																																																											
Sample #	Measured	Required	Dielectric breakdown			Result																																																												
	MΩ	MΩ	Yes (description)		No																																																													
2	>999	25	--		X	P																																																												
Supplementary information: Size of module 1,64 m <sup>2</sup>																																																																		